

Title: Seidl: Virtual\_Machines (18.05.2015)

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Pages: 28

```

codeV(fun x0...xk-1 → e) ρ sd =
  getvar z0 ρ sd
  getvar z1 ρ (sd + 1)
  ...
  getvar zg-1 ρ (sd + g - 1)
  mkvec g
  mkfunval A
  jump B
A : targ k
  codeV e ρ' 0
  return k
B : ...
    
```

where  $\{z_0, \dots, z_{g-1}\} = \text{free}(\text{fun } x_0 \dots x_{k-1} \rightarrow e)$   
 and  $\rho' = \{x_i \mapsto (L, -i) \mid i = 0, \dots, k-1\} \cup \{z_j \mapsto (G, j) \mid j = 0, \dots, g-1\}$

## 16 Function Definitions

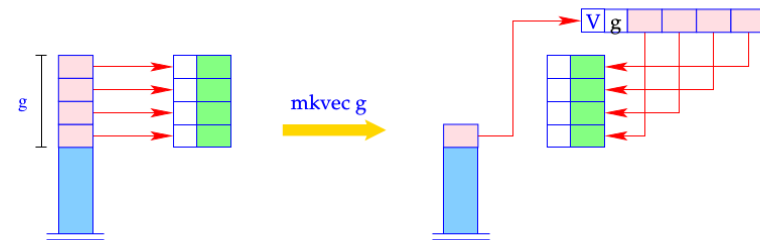
The definition of a function  $f$  requires code that allocates a **functional value** for  $f$  in the heap. This happens in the following steps:

- Creation of a Global Vector with the binding of the free variables;
- Creation of an (initially empty) argument vector;
- Creation of an F-Object, containing references to these vectors and the start address of the code for the body;

Separately, code for the body has to be generated.

Thus:

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```

h = new (V, n);
SP = SP - g + 1;
for (i=0; i<g; i++)
  h->v[i] = S[SP + i];
S[SP] = h;
    
```

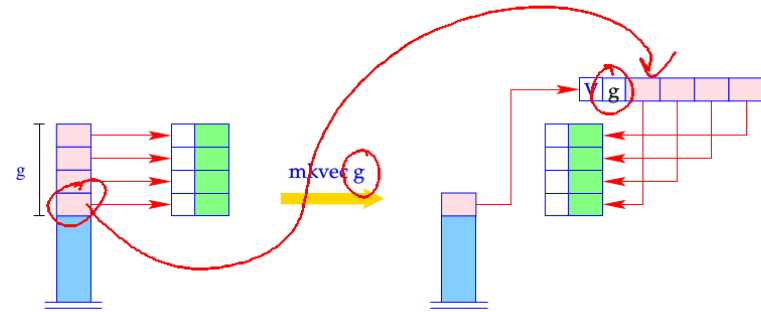
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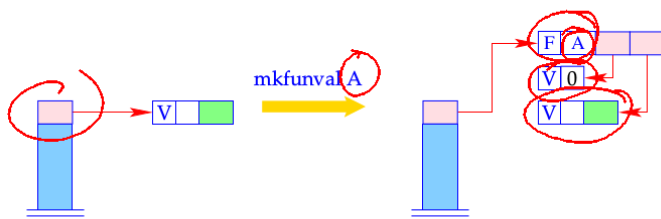


```

h = new (V, n);
SP = SP - g + 1;
for (i=0; i<g; i++)
  h->v[i] = S[SP + i];
S[SP] = h;

```

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```

a = new (V, 0);
S[SP] = new (F, A, a, S[SP]);

```

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### Example

Regard  $f \equiv \text{fun } b \rightarrow a + b$  for  $\rho = \{a \mapsto (L, 1)\}$  and  $sd = 1$ .

code<sub>V</sub> f ρ 1 produces:

|   |            |   |            |   |          |
|---|------------|---|------------|---|----------|
| 1 | pushloc 0  | 0 | pushglob 0 | 2 | getbasic |
| 2 | mkvec 1    | 1 | eval       | 2 | add      |
| 2 | mkfunval A | 1 | getbasic   | 1 | mkbasic  |
| 2 | jump B     | → | pushloc 1  | 1 | return 1 |
| 0 | A : targ 1 | 2 | eval       | 2 | B : ...  |


The secrets around **targ k** and **return k** will be revealed later :-)

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## 17 Function Application

Function applications correspond to function calls in C.

The necessary actions for the evaluation of  $e' e_0 \dots e_{m-1}$  are:

- Allocation of a stack frame; 
- Transfer of the actual parameters, i.e. with:
  - CBV:** Evaluation of the actual parameters;
  - CBN:** Allocation of closures for the actual parameters;
- Evaluation of the expression  $e'$  to an F-object;
- Application of the function.

Thus for CBN:

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$$S = \{ a \mapsto (L, 1) \} f \mapsto (L, 2)$$

### A Slightly Larger Example

let  $a = 17$  in let  $f = \text{fun } b \rightarrow a + b$  in  $f$  42

For **CBV** and  $sd = 0$  we obtain:

|   |            |   |            |   |           |   |            |
|---|------------|---|------------|---|-----------|---|------------|
| 0 | loadc 17   | 2 | jump B     | 2 | getbasic  | 5 | loadc 42   |
| 1 | mkbasic    | 0 | A: targ 1  | 2 | add       | 5 | mkbasic    |
| 1 | pushloc 0  | 0 | pushglob 0 | 1 | mkbasic   | 6 | pushloc 4  |
| 2 | mkvec 1    | 1 | getbasic   | 1 | return 1  | 7 | apply      |
| 2 | mkfunval A | 1 | pushloc 1  | 2 | B: mark C | 3 | C: slide 2 |

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```

codeV (e' e0 ... em-1) ρ sd = mark A           // Allocation of the frame
                               codeC em-1 ρ (sd + 3)
                               codeC em-2 ρ (sd + 4)
                               ...
                               codeC e0 ρ (sd + m + 2)
                               codeV e' ρ (sd + m + 3) // Evaluation of e'
                               apply                // corresponds to call
A: ...
    
```

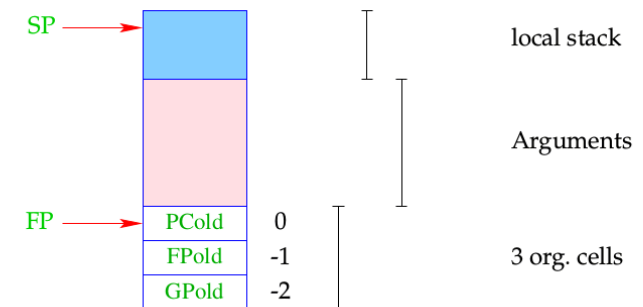
To implement **CBV**, we use `codeV` instead of `codeC` for the arguments  $e_i$ .

**Example** For  $(f$  42),  $\rho = \{f \mapsto (L, 2)\}$  and  $sd = 2$ , we obtain with **CBV**:

|   |          |   |           |   |        |
|---|----------|---|-----------|---|--------|
| 2 | mark A   | 6 | mkbasic   | 7 | apply  |
| 5 | loadc 42 | 6 | pushloc 4 | 3 | A: ... |

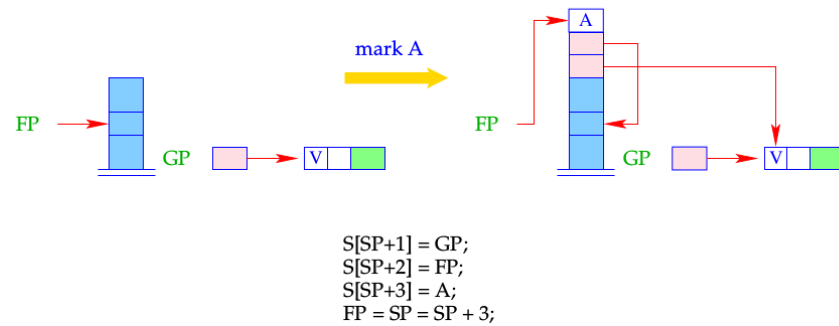
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For the implementation of the new instruction, we must fix the organization of a stack frame:



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Different from the **CMa**, the instruction **mark A** already saves the return address:



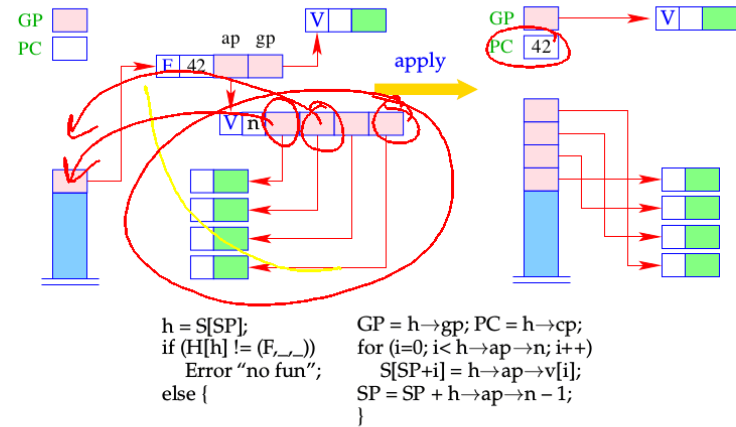
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### Caveat

- The last element of the argument vector is the last to be put onto the stack. This must be the **first** argument reference.
- This should be kept in mind, when we treat the packing of arguments of an under-supplied function application into an F-object !!!

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The instruction **apply** unpacks the F-object, a reference to which (hopefully) resides on top of the stack, and continues execution at the address given there:



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## 18 Over- and Undersupply of Arguments

The first instruction to be executed when entering a function body, i.e., after an **apply** is **targ k**.

This instruction checks whether there are enough arguments to evaluate the body.

Only if this is the case, the execution of the code for the body is started.

Otherwise, i.e. in the case of **under-supply**, a new F-object is returned.

The test for number of arguments uses:  $SP - FP$

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`targ k` is a complex instruction.

We decompose its execution in the case of `under-supply` into several steps:

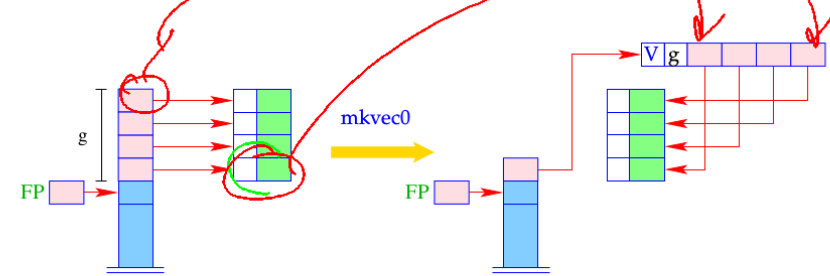
```

targ k = if (SP - FP < k) {
    mkvec0;    // creating the argumentvector
    wrap;      // wrapping into an F - object
    popenv;    // popping the stack frame
}
    
```

The combination of these steps into one instruction is a kind of optimization :-)

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The instruction `mkvec0` takes all references from the stack above `FP` and stores them into a vector:

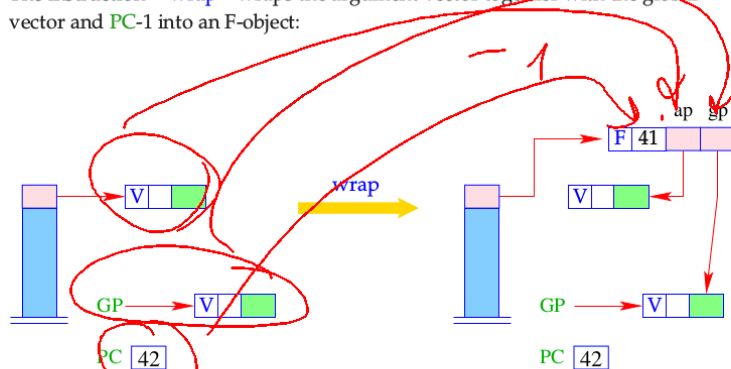


```

g = SP - FP; h = new (V, g);
SP = FP + 1;
for (i=0; i < g; i++)
    h->v[i] = S[SP + i];
S[SP] = h;
    
```

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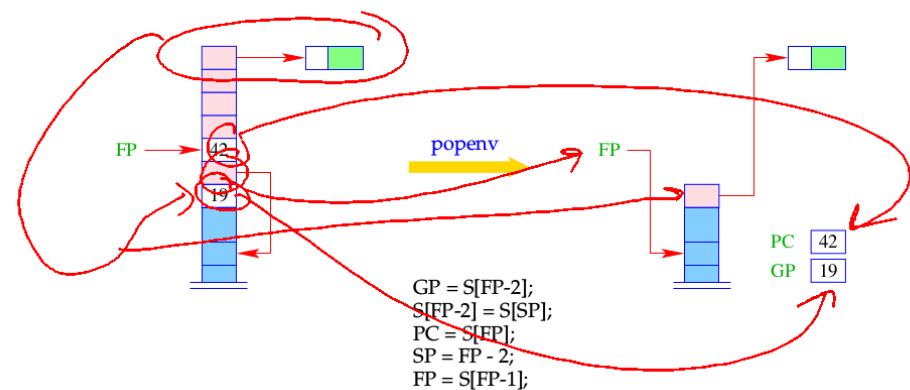
The instruction `wrap` wraps the argument vector together with the global vector and `PC-1` into an F-object:



$S[SP] = \text{new}(F, PC-1, S[SP], GP);$

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The instruction `popenv` finally releases the stack frame:

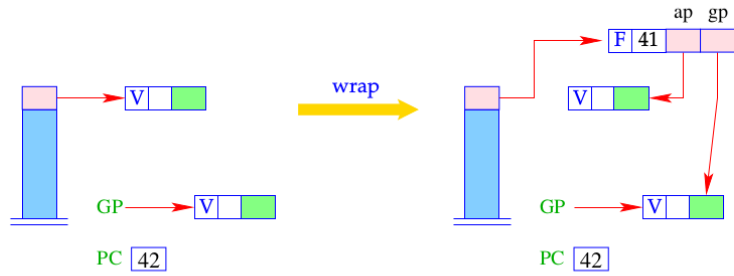


```

GP = S[FP-2];
S[FP-2] = S[SP];
PC = S[FP];
SP = FP - 2;
FP = S[FP-1];
    
```

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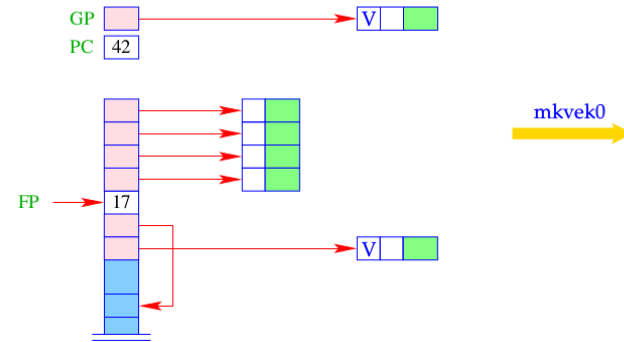
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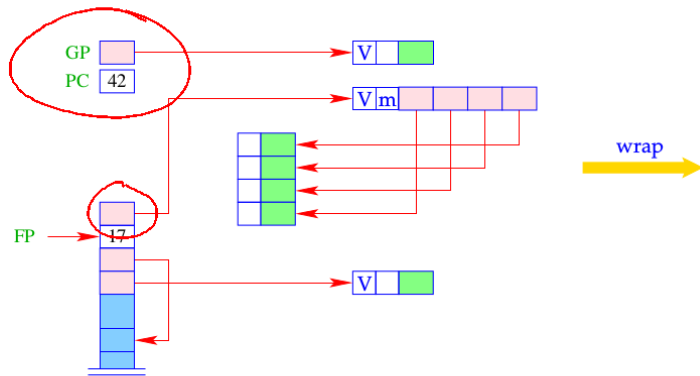
$S[SP] = \text{new}(F, PC-1, S[SP], GP);$

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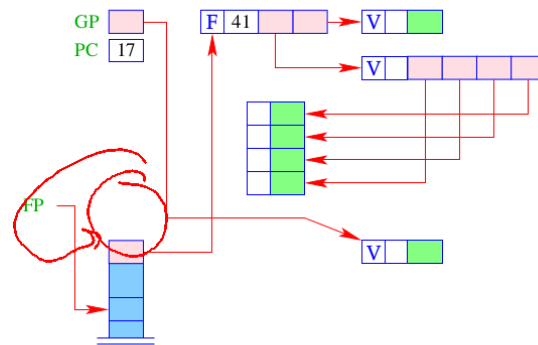
Thus, we obtain for `targ k` in the case of under supply:



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- The stack frame can be released **after the execution of the body** if exactly the right number of arguments was available.
- If there is an **oversupply** of arguments, the body must evaluate to a function, which consumes the rest of the arguments ...
- The check for this is done by **return k**:

```

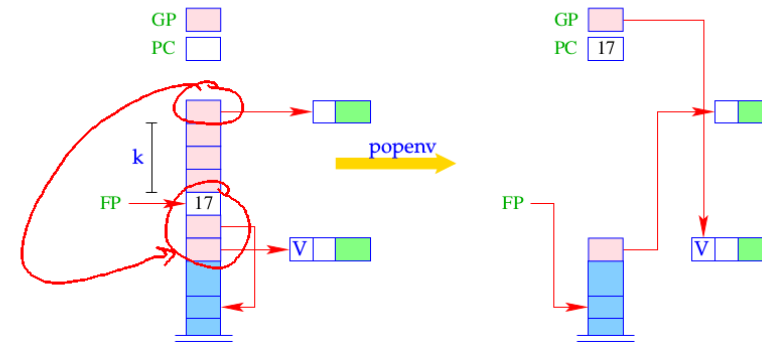
return k = if (SP - FP = k + 1)
    popenv;           // Done
  else {             // There are more arguments
    slide k;
    apply;           // another application
  }

```

The execution of **return k** results in:

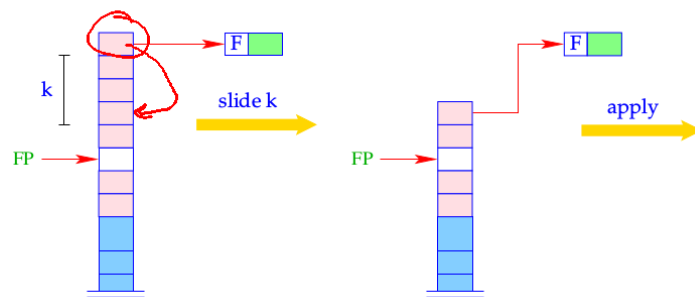
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Case: Done



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Case: Over-supply



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## 19 let-rec-Expressions

Consider the expression  $e \equiv \text{let rec } y_1 = e_1 \text{ and } \dots \text{ and } y_n = e_n \text{ in } e_0$  .

The translation of  $e$  must deliver an instruction sequence that

- allocates local variables  $y_1, \dots, y_n$ ;
- in the case of
  - CBV**: evaluates  $e_1, \dots, e_n$  and binds the  $y_i$  to their values;
  - CBN**: constructs closures for the  $e_1, \dots, e_n$  and binds the  $y_i$  to them;
- evaluates the expression  $e_0$  and returns its value.

### Caveat

In a **letrec**-expression, the definitions can use variables that will be allocated **only later!**  $\implies$  **Dummy-values** are put onto the stack before processing the definition.

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For **CBN**, we obtain:

```
codeV e ρ sd = alloc n           // allocates local variables
               codeC e1 ρ' (sd + n)
               rewrite n
               ...
               codeC en ρ' (sd + n)
               rewrite 1
               codeV e0 ρ' (sd + n)
               slide n           // deallocates local variables
```

where  $\rho' = \rho \oplus \{y_i \mapsto (L, \text{sd} + i) \mid i = 1, \dots, n\}$ .

In the case of **CBV**, we also use `codeV` for the expressions  $e_1, \dots, e_n$ .

### Caveat

Recursive definitions of basic values are **undefined** with **CBV!!!**